

Table 6-5
I/O memory switches

Name	Function	Location		Notes
		Hex	Decimal	
SLOT3ROM	Slot ROM at \$C300	\$C00B	49163 -16373	Write
	Internal ROM at \$C300	\$C00A	49162 -16374	Write
	Read SLOT3ROM switch	\$C017	49175 -16361	Read
SLOT3XROM	Slot ROM at \$Cx00	\$C006	49159 -16377	Write
	Internal ROM at \$Cx00	\$C007	49158 -16378	Write
	Read SLOT3XROM switch	\$C015	49173 -16363	Read

When SLOT3ROM is on, the 256-byte ROM area at \$C300 is available to a peripheral card in slot 3, which is the slot normally used for a terminal interface. If a card is installed in the auxiliary slot when you turn on the power or reset the Apple IIe, the SLOT3ROM switch is turned off. Turning SLOT3ROM off disables peripheral-card ROM in slot 3 and enables the built-in 80-column firmware, as shown in Figure 6-3. The 80-column firmware is assigned to slot-3 address space because slot 3 is normally used with a terminal interface, so the built-in firmware will work with programs that use slot 3 this way.

The bus and I/O signals are always available to a peripheral card in slot 3, even when the 80-column hardware and firmware are operating. Thus it is always possible to use this slot for any I/O peripheral that does *not* have built-in firmware.

When SLOT3XROM is active (high), the I/O memory space from \$C100 to \$C7FF is allocated to the expansion slots, as described previously. Setting SLOT3XROM inactive (low) disables the peripheral-card ROM and selects built-in ROM in all of the I/O memory space except the part from \$C000 to \$C0FF (used for soft switches and data I/O), as shown in Figure 6-3. In addition to the 80-column firmware at \$C300 and \$C800, the built-in ROM includes firmware that performs the self-test of the Apple IIe's hardware.

❖ *Note:* Setting SLOT3XROM low enables built-in ROM in all of the I/O memory space (except the soft-switch area), including the \$C300 space, which contains the 80-column firmware.